

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of S.N. 08/970,212:

Shunji NAKAMURA

Serial No.: NEW

Filed: December 6, 2001

For: SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR FABRICATING
THE SAME

11/2 Group Art Unit: 3811 (Expected)

Examiner: NADA V. O. (Expected)
Gotoh



INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
Washington, D.C. 20231

December 6, 2001

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 08/970,212 and grandparent application Serial No. 08/660,324.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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